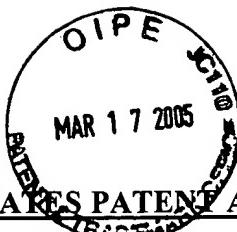
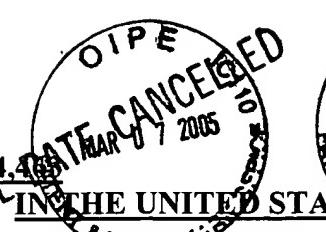


S/N 09/644,463



884.2111 \$

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew B. Haycock et al.

Examiner: Raymond Phan

Serial No.: 09/644,463

Group Art Unit: 2111

Filed: August 23, 2000

Docket No.: 884.303US1

Title: SIMULTANEOUS BIDIRECTIONAL PORT WITH SYNCHRONIZATION  
CIRCUIT TO SYNCHRONIZE THE PORT WITH ANOTHER PORT

Assignee: Intel Corporation

Customer No: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Office Action dated 15 November 2004, the applicants request reconsideration of the above-identified application in view of the following.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from 15 February 2005 to 15 March 2005.